Question Paper Code: 86566

B.E./B.Tech. DEGREE EXAMINATIONS, NOVEMBER/DECEMBER 2021.

Third Semester

Electronics and Communication Engineering

EC 1203 — ELECTRONIC CIRCUITS – I

(Regulations 2008)

Time: Three hours

Maximum: 100 marks

Answer ALL questions.

PART A —
$$(10 \times 2 = 20 \text{ marks})$$

- 1. Define Q-point.
- 2. What are advantages of self bias over other types of biasing?
- 3. Compare the performance of CE, CB, CC amplifiers with respect to voltage and current gain.
- 4. State Miller's theorem.
- 5. Mention the relationship between bandwidth and rise time.
- 6. Write the difference between single stage and multistage amplifiers.
- 7. Why non-linear distortion is called harmonic distortion?
- 8. What is thermal resistance? And give its unit.
- 9. Distinguish between voltage and current feedback connection.
- 10. What is the effect of negative feedback on gain and bandwidth?

PART B —
$$(5 \times 16 = 80 \text{ marks})$$

11. (a) Draw a voltage divider bias BJT network and derive all the stability factors S, S' and S". (16)

(b)	(i)	Explain the methods of stabilizing the Q point.	(8)
	(ii)	Explain the thermister and sensistor compensation.	(8)
bias usin		using hybrid parameters model and derive the equations f	
		Or	
(b)	with	th neat circuit diagram. And also derive the expressions t	
(a)	Discuss the high frequency equivalent circuit of FET and gain bandwidth product for any one configuration.		ee derive (16)
		${ m Or}$	
(b)	List precautions that should be observed for operational amplifier circuit stability. Briefly explain in each case. (16)		
(a)	(i)	With neat circuit diagram explain transformer coupled Audio power amplifier.	class A (8)
	(ii)	Determine the efficiency of class A amplifier.	(8)
		${ m Or}$	
(b)	(i)	With neat circuit diagram explain class B push pull amplific	er. (8)
	(ii)	What are the different types of distortion in amplifiers? Exp	olain. (8)
(a)	(i) (ii)	Bandwidth Distortion	
	(iv)	•	$\times 4 = 16$)
		Or	
(b)	(i)	With a neat sketch explain what type of feedback is emp Emitter follower? How much negative feedback is used?	oloyed in (8)
	(ii)	With the aid of input and output circuit of an Emitter obtain the expression for its resultant voltage gain.	follower, (8)
	(a) (b) (a) (b) (a)	(ii) (a) Drawbias imposite im	(ii) Explain the thermister and sensistor compensation. (a) Draw the AC equivalent circuit of a CE amplifier with voltage bias using hybrid parameters model and derive the equations for impedance, output impedance, voltage gain and current gain. Or (b) Explain an emitter coupled differential amplifier and its salient with neat circuit diagram. And also derive the expressions for input impedance and output impedance. (a) Discuss the high frequency equivalent circuit of FET and hence gain bandwidth product for any one configuration. Or (b) List precautions that should be observed for operational amplifier stability. Briefly explain in each case. (a) (i) With neat circuit diagram explain transformer coupled Audio power amplifier. Or (b) (i) With neat circuit diagram explain class B push pull amplifier (ii) What are the different types of distortion in amplifiers? Explain Discuss in detail the effect of negative feedback on the following (i) Bandwidth (ii) Distortion (iii) Stability (iv) Input and output impedances. Or (b) (i) With a neat sketch explain what type of feedback is emp Emitter follower? How much negative feedback is used? (ii) With the aid of input and output circuit of an Emitter

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